
(19)

KOREAN INTELLECTUAL PROPERTY OFFICE

KOREAN PATENT ABSTRACTS

(11)Publication number: 1019990045059 A
(43)Date of publication of application: 25.06.1999

(21)Application number: 1019980047422 (71)Applicant: NEC CORPORATION
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(30)Priority: 07.11.1997 JP1997 KAZUO
322068
(51)Int. Cl G06F 12/00

(54) CACHE MEMORY SYSTEM, ESPECIALLY FOR PROVIDING THE CACHE MEMORY SYSTEM CAPABLE OF RENEWING THE CACHE MEMORY FOR A USER WITHOUT ADDING A SPECIAL HARDWARE

(57) Abstract:

PURPOSE: A cache memory system is provided to reduce the size of a circuit and prevent the size increase of the hardware system, thereby freely setting the renewal time.

CONSTITUTION: A cache memory system comprising of a memory with a plurality of banks and a cache controller(2) transmitting the cache renewal order by an instruction of CPU(1) to a DMA(Direct memory access) controller(4) constitutes the followings: the DMA controller transmitting the data between the main memory(5) and the cache memory(3) according to the received order from the cache controller.